FIG.1

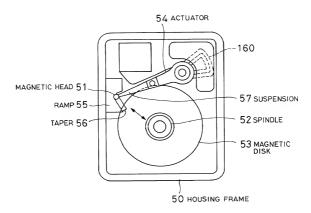


FIG.2

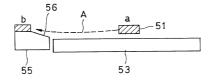
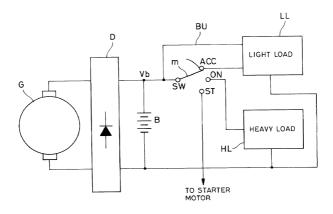


FIG.3



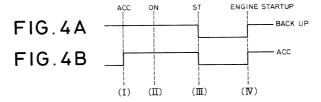
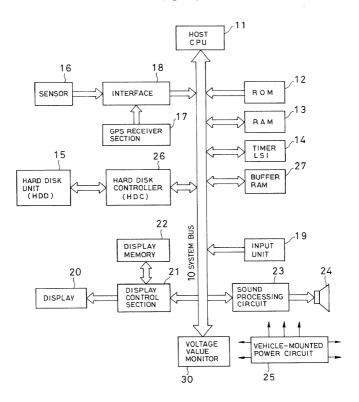
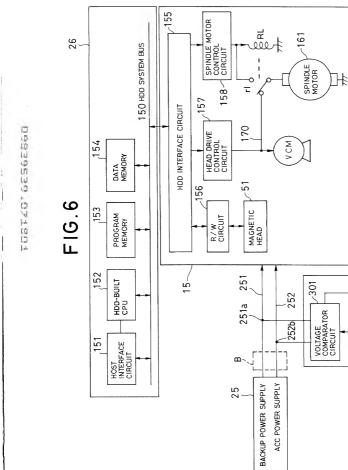


FIG.5





► SYSTEM BUS 10

~305

THRESHOLD SET CIRCUIT

30

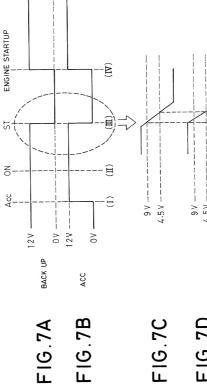


FIG.7D

FIG.8A

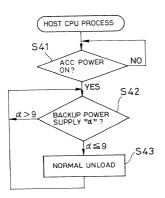


FIG.8B

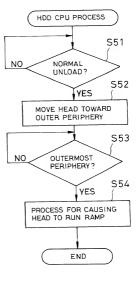


FIG.9A

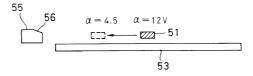




FIG.9B

